

## Cypress First Touch I/O Module - Pinout

Specific designations and features of the First Touch Kit 3 as they apply to FRC are listed in the following tables.

<b>P2 “Wireless” connector</b>		<b>Notes</b>
Pin #	Name	
12	AO 2	For Analog Output 1 & 2, 0-4V, 100uA supply current
11	AO 1	
10	Button 5	Capacitive Touch Button Input
9	GND	
8	Button 4	Capacitive Touch Button Input
7	Button 6	Capacitive Touch Button Input
6	Button 3	Capacitive Touch Button Input
5	HC DO 1	HighCurrent DigitalOut 1, 3.3V, 4mA source, 25mA sink
4	Button 2	Capacitive Touch Button Input
3	HC DO 2	HighCurrent DigitalOut 2, 3.3V, 4mA source, 25mA sink
2	3.3V	
1	GND	

<u>J2</u>		<u>J3</u>		Notes	
Marking	API Name	Marking	API Name		
VDDIO	V+	VDDIO	V+	<p>VDD IO interface voltage</p> <ul style="list-style-type: none"> <li>- V+ output @ 3.3V (recommended): <ul style="list-style-type: none"> <li>o Jump pins 2,3 on J1 and pins 2,3 on J4</li> <li>o On-board regulator, 9V Battery current boost, same as Analog</li> </ul> </li> <li>- V+ output @ 5V: <ul style="list-style-type: none"> <li>o Jump pins 1,2 on J1 and pins 1,2 on J4</li> <li>o Powered directly from USB Vbus</li> </ul> </li> <li>- Sourced externally, custom (discouraged): <ul style="list-style-type: none"> <li>o Leave J1 open, jump pins 1,2 on J4</li> </ul> </li> </ul> <p>Interface voltage supplied to V+ pins on J2 and J3</p>	
P0_0	AI 1	P0_1	AI 2	- 14-bit, 0-3.3V	
P0_2	AI 3	P0_3	AI 4		
P0_4	AI 5	P0_5	AI 6		
P0_6	AI 7	P0_7	AI 8		
P4_4	Digital 1	P4_5	Digital 2	<ul style="list-style-type: none"> <li>- 2 true PWM generators available</li> <li>- Each PWM has configurable frequency and 2 outputs (independent duty cycle)</li> <li>- 16-bits</li> <li>- 24MHz time-base</li> <li>PWM 1.Output 1: Digital 1 Output 2: Digital 2</li> <li>PWM 2.Output 1: Digital 3 Output 2: Digital 4</li> </ul>	<p>All digital lines are configurable</p> <ul style="list-style-type: none"> <li>- Open-drain input</li> <li>- 5kΩ pull-up input</li> <li>- 5kΩ pull-down input</li> <li>- Active drive output <ul style="list-style-type: none"> <li>o 4mA current source</li> <li>o 8mA current sink</li> </ul> </li> </ul>
P4_6	Digital 3	P4_7	Digital 4		
P6_0	Digital 5	P6_1	Digital 6	<ul style="list-style-type: none"> <li>- 2 Quadrature decoders (4X decoding) available</li> <li>- Optional encoder index input</li> <li>Quad 1.A: Digital 5 B: Digital 7 Index: Digital 9</li> <li>Quad 2.A: Digital 6 B: Digital 8 Index: Digital 10</li> </ul>	
P6_2	Digital 7	P6_3	Digital 8		
P6_4	Digital 9	P6_5	Digital 10		
P6_6	Digital 11	P6_7	Digital 12		
P12_2	Digital 13	P12_3	Digital 14	<ul style="list-style-type: none"> <li>- High current sink (25mA) output</li> <li>- Same specs as HC DOs on P2, but high is Vddio instead of 3.3V</li> </ul>	
P2_6	Digital 15	P2_7	Digital 16	<ul style="list-style-type: none"> <li>- Analog comparator inputs available</li> <li>- Reference voltage generated by AO 1</li> </ul>	
GND	GND	GND	GND		

## Other

- The physical button on the board is Button1 in the API. If pressed before the Driver Station application is run, the firmware version is displayed in binary on the LEDs.
- The 8 LEDs on the board are available in the Enhanced API. In compatibility mode, they mirror the Digital Output lines.
- The Capacitive Touch slider on the board is available in the Enhanced API.
- The 3 axis accelerometer is available in the Enhanced API.